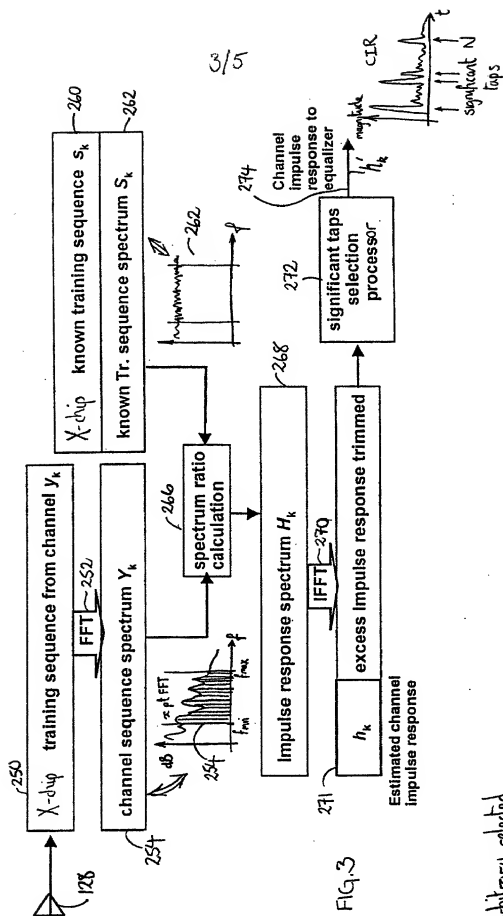


FIG. 4



X = arbitrary selected
number of chips in training
sequence set by system standard.

500

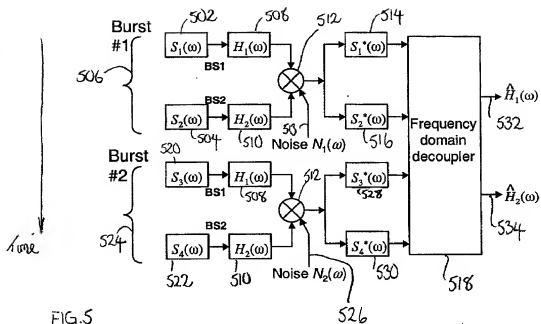


FIG. 5

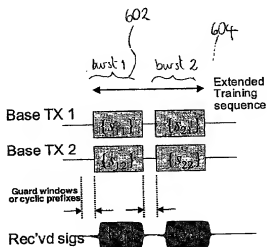


FIG. 6

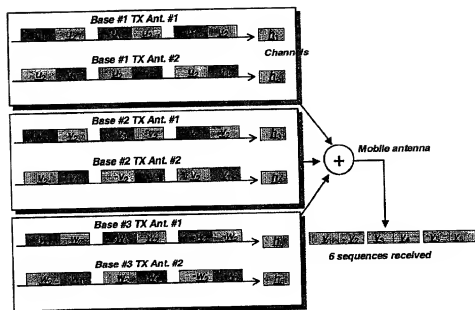


FIG. 7